

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Original) A soldering method comprising the steps of:

 bonding a first electronic component having electrodes plated with a material containing lead to one surface of an interconnect substrate through solder containing no lead;

 flow-soldering to bond a second electronic component to the other surface of the interconnect substrate; and

 heating a joint section between the first electronic component and the interconnect substrate at the same time as or after the step of flow-soldering to melt the joint section.
2. (Original) The soldering method as defined in claim 1,

 wherein the step of heating the joint section is performed at the same time as the step of flow soldering.
3. (Original) The soldering method as defined in claim 1,

 wherein the step of heating the joint section is performed after the step of flow soldering.
4. (Original) The soldering method as defined in claim 1,

 wherein the step of heating the joint section is performed both at the same time as and after the step of flow soldering.
5. (Original) The soldering method as defined in claim 1,

 wherein the step of heating the joint section is performed by at least one of radiant heat and a hot blast.
6. (Original) The soldering method as defined in claim 1,

 wherein the first electronic component is bonded to one surface of the interconnect substrate by reflow soldering.

7. (Original) The soldering method as defined in claim 1,
wherein the first electronic component is bonded to one surface of the interconnect substrate by hand soldering.
8. (Original) The soldering method as defined in claim 1, further comprising:
a step of preheating the joint section before the step of flow soldering.
9. (Original) The soldering method as defined in claim 1,
wherein the solder containing no lead is formed of at least one material selected from a group consisting of tin, silver, copper, zinc, and bismuth.
10. (Original) The soldering method as defined in claim 1,
wherein at least the step of heating the joint section is performed in a chamber.
11. (Original) A method of fabricating an electronic circuit module,
wherein the first and second electronic components are mounted on the interconnect substrate by the soldering method as defined in claim 1.
12. (Currently Amended) A soldering device comprising:
a flow soldering ~~section,~~section for bonding a second electronic component to an interconnect substrate to which ~~when a first electronic component having electrodes plated with a material containing lead is bonded, the first electronic component being bonded~~ to one surface of an interconnect substrate through solder containing no lead, ~~bonds a~~ the second electronic component being bonded to the other surface of the interconnect ~~substrate, substrate;~~
- wherein the flow soldering section includes a heater disposed on the side of a ~~the one~~ surface of the interconnect ~~substrate~~substrate; ~~to which the first electronic component is bonded, to melt a joint section between the first electronic component and the interconnect~~ substrate

wherein the flow soldering section has a solder supplying section disposed on the side of the other surface of the interconnect substrate; and

wherein the heater is disposed right above the solder supplying section such that the heater melts a joint section between the first electronic component and the interconnect substrate at the same time that the second electronic component is bonded to the interconnect substrate.

13. (Canceled)

14. (Currently Amended) ~~The soldering device as defined in claim 12,~~ A soldering device comprising:

a flow soldering section for bonding a second electronic component to an interconnect substrate to which a first electronic component having electrodes plated with a material containing lead is bonded, the first electronic component being bonded to one surface of an interconnect substrate through solder containing no lead, the second electronic component being bonded to the other surface of the interconnect substrate;

wherein the flow soldering section includes a heater disposed on the side of the one surface of the interconnect substrate;

wherein the flow soldering section has a solder supplying section disposed on the side of the other surface of the interconnect substrate; and

wherein the heater is disposed downstream from the solder supplying section in a direction in which the interconnect substrate is transferred such that the heater melts a joint section between the first electronic component and the interconnect substrate after the second electronic component is bonded to the interconnect substrate.

15. (Currently Amended) ~~The soldering device as defined in claim 12,~~ A soldering device comprising:

a flow soldering section for bonding a second electronic component to an interconnect substrate to which a first electronic component having electrodes plated with a material containing lead is bonded, the first electronic component being bonded to one surface of an interconnect substrate through solder containing no lead, the second electronic component being bonded to the other surface of the interconnect substrate;

wherein the flow soldering section includes a heater disposed on the side of the one surface of the interconnect substrate;

wherein the flow soldering section has a solder supplying section disposed on the side of the other surface of the interconnect substrate; and

wherein the heater is disposed right above the solder supplying section ~~or~~ and downstream from the solder supplying section in a direction in which the interconnect substrate is transferred such that the heater melts a joint section between the first electronic component and the interconnect substrate at the same time or after the second electronic component is bonded to the interconnect substrate.

16. (Currently Amended) ~~The soldering device as defined in claim 12,~~ A soldering device comprising:

a flow soldering section for bonding a second electronic component to an interconnect substrate to which a first electronic component having electrodes plated with a material containing lead is bonded, the first electronic component being bonded to one surface of an interconnect substrate through solder containing no lead, the second electronic component being bonded to the other surface of the interconnect substrate;

wherein the flow soldering section includes a plurality of the heaters disposed on the side of the one surface of the interconnect substrate;

~~comprising a plurality of the heaters;~~

wherein the flow soldering section has a solder supplying section disposed on the side of the other surface of the interconnect substrate;

wherein part of the heaters is disposed right above the solder supplying section; and
 ———wherein another part of the heaters is disposed downstream from the solder supplying section in a direction in which the interconnect substrate is transferred such that the heaters melt a joint section between the first electronic component and the interconnect substrate at the same time or after the second electronic component is bonded to the interconnect substrate.

17. (Original) The soldering device as defined in claim 12, comprising:

a plurality of the heaters, wherein at least one of the heaters is a far infrared heater.

18. (Original) The soldering device as defined in claim 12, further comprising a fan.

19. (Original) The soldering device as defined in claim 12, further comprising:

a reflow soldering section for bonding the first electronic component to one surface of the interconnect substrate.

20. (Original) The soldering device as defined in claim 12, further comprising:

a second heater for preheating the joint section before flow soldering.

21. (Original) The soldering device as defined in claim 12, further comprising:

a chamber in which at least the flow soldering section is disposed.

22. (Original) A device of fabricating an electronic circuit module comprising the soldering device as defined in claim 12,

wherein the first and second electronic components are mounted on the interconnect substrate.